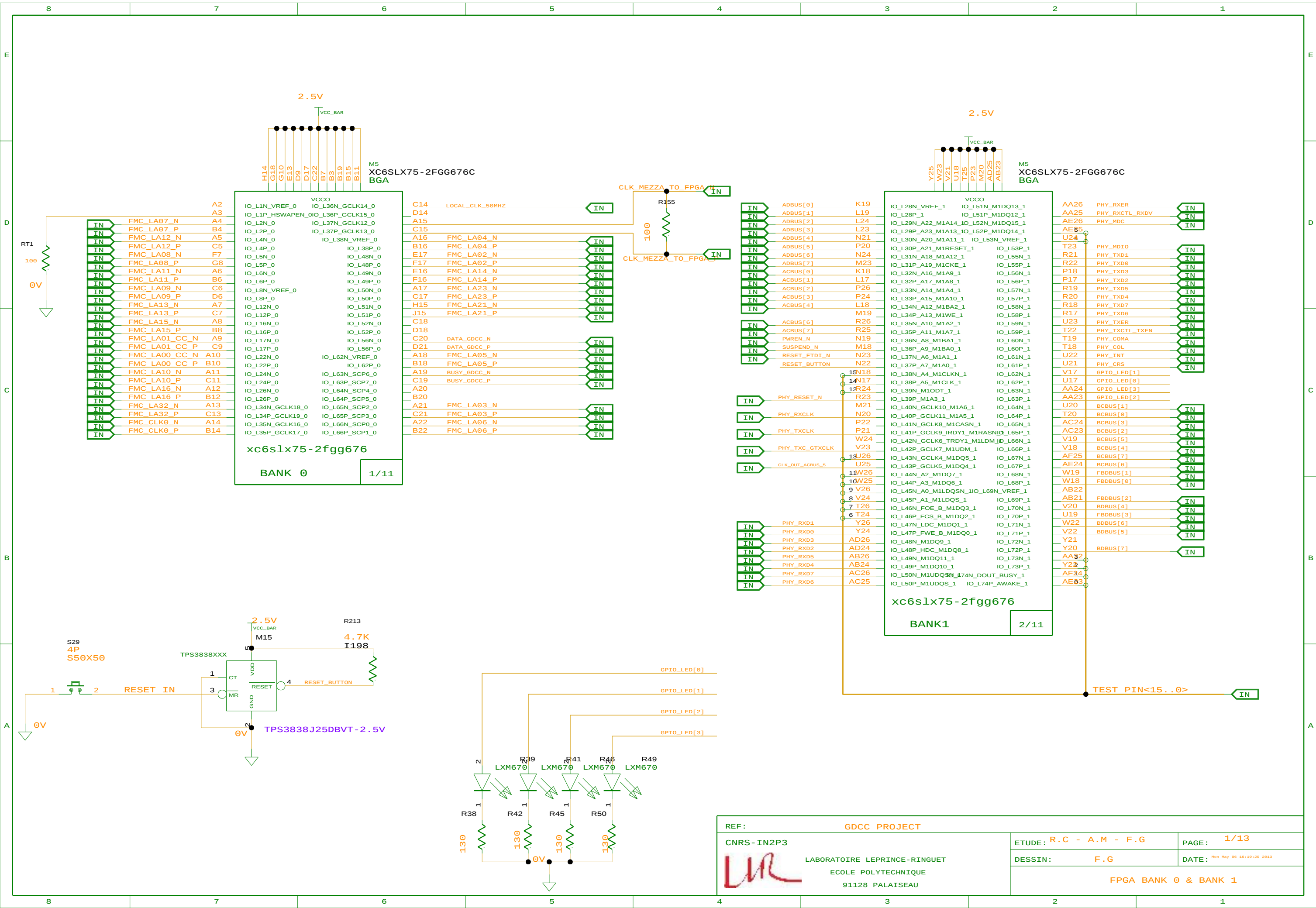
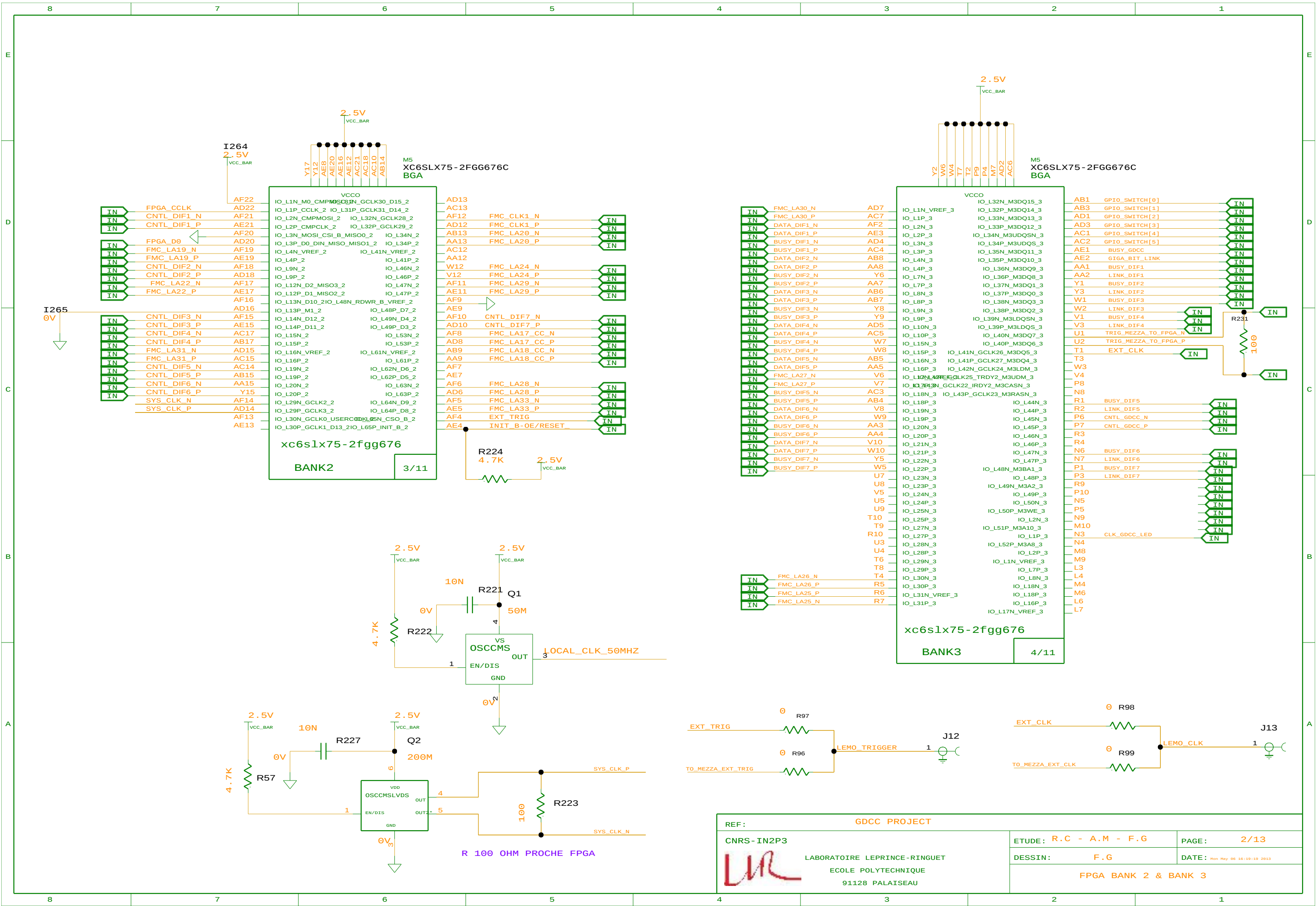
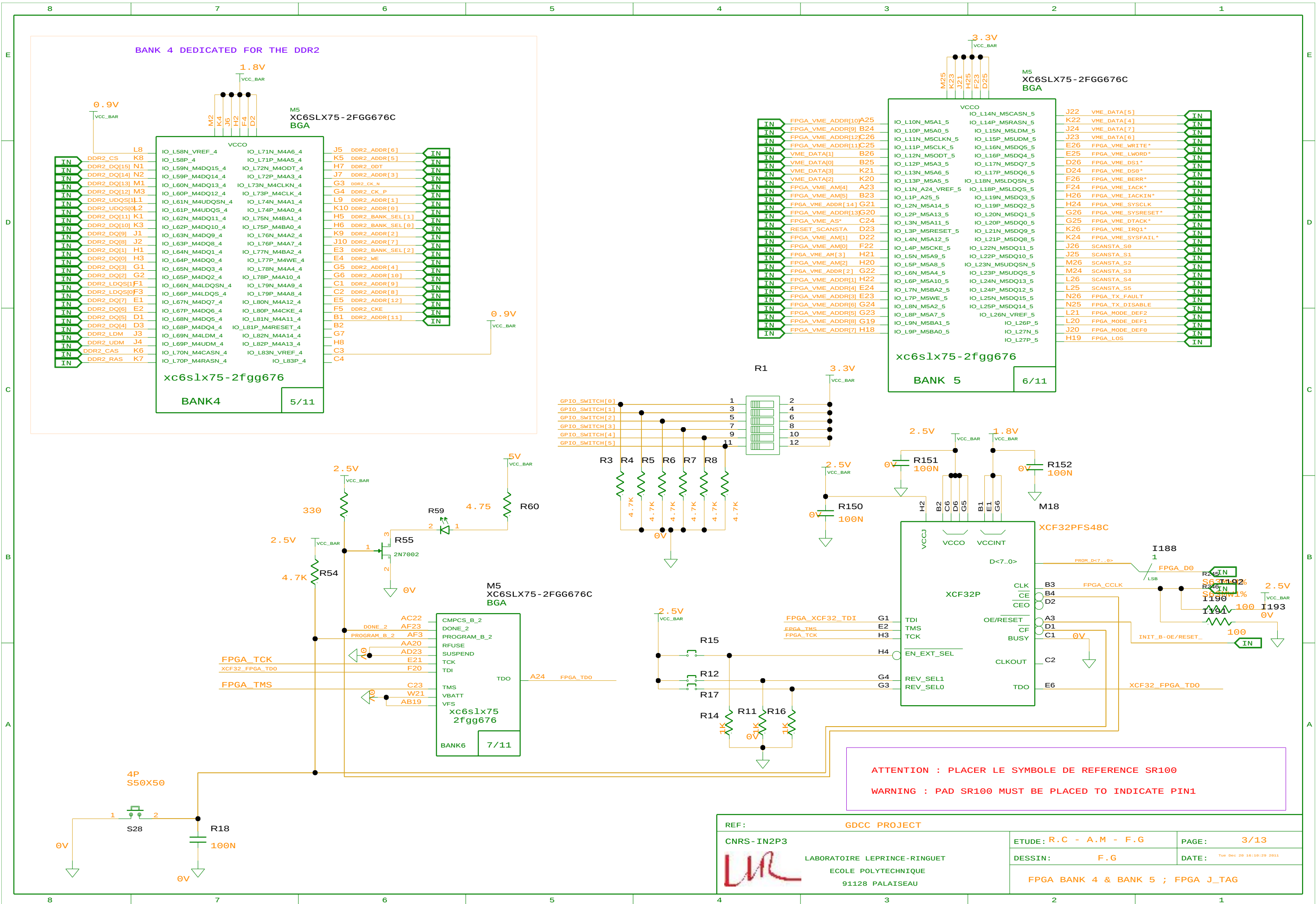
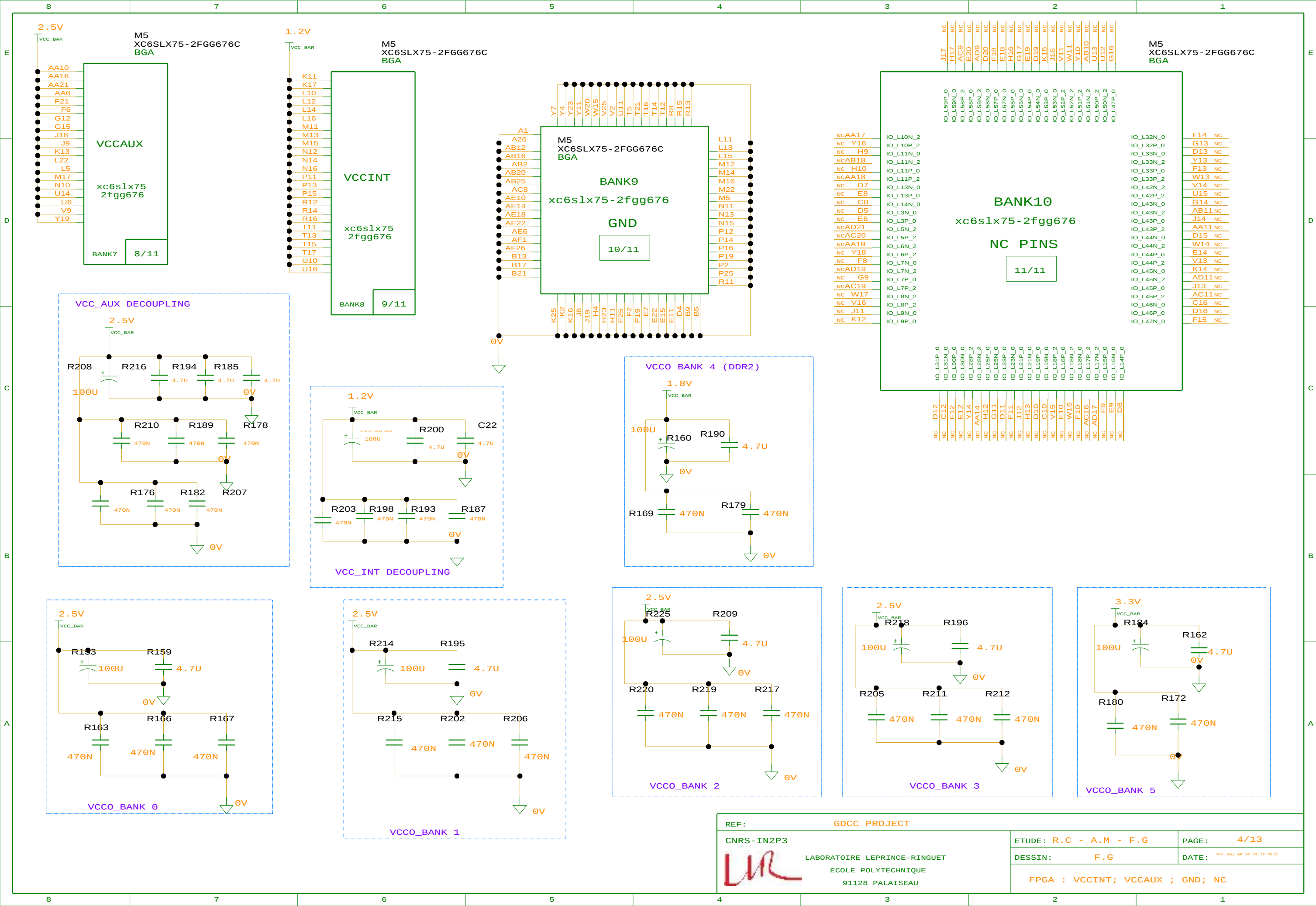










THIS PAGE IS ADDED TO INDICATE THE MARVELL CONFIGURATION PIN

PIN TO CONSTANT MAPPING	
PIN	BIT[2:0]
VCC2V5	111
LED_LINK10	110
LED_LINK100	101
LED_LINK1000	100
LED_DUPLEX	011
LED_RX	010
LED_TX	001
GND	000

PIN	BIT[2]	BIT[1]	BIT[0]	VALUE	DESCRIPTION
CFG0	PHYADR[2]	PHYADR[1]	PHYADR[0]	111	PHYADDR 00111
CFG1	ENA_PAUSE	PHYADR[4]	PHYADR[3]	000	DON'T ADVERTISE THE PAUSE BIT
CFG2	ANEG[3]	ANEG[2]	ANEG[1]	111	AUTO NEG ENABLE, ADVERTISE ALL CAPABILITIES AND PREFER MASTER
CFG3	ANEG[0]	ENA_XC	DIS_125	011	AUTO CROSSOVER ENABLE DISABLE 125 CLK
CFG4	HWCFG_MD[2]	HWCFG_MD[1]	HWCFG_MD[0]	111	GMII TO COPPER MODE
CFG5	DIS_FC	DIS_SLEEP	HWCFG_MD[3]	001	FIBER/COPPER AUTODETECT ENABLE, .SLEEP MODE DISABLE
CFG6	SEL_BDT	INT_POL	75/50 OHM	010	MDC/MDIO SELECTED,ACTIVE HIGH INTERRUPT, AND 50 OHM SERDES OPTION

