

To : SYNOPSIS

Palaiseau, France, November 20th 2015

Object: End Usage Statement for Synopsys SFPGA licenses via EUROPRACTICE



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I confirm that the strictly non-commercial and University like end usage intentions for the above tools, which my Institution would like to acquire via EUROPRACTICE, are as follows:

- Preparation of educational material (examples, test case, trainings): lessons and trainings are given each year by 2 members of the team at Master level (about 100 hours of lessons given at Ecole Polytechnique and Blaise Pascal University) in the field of system control, automation, digital signal processing, modeling and mixed system simulation. The studied electronics systems architectures are based on FPGAs and/or ASICs.
- Student internship and/or class projects: the team welcomes 2 to 3 students each year for their MSc internship or class project. The purpose of the internship usually consists in developing and testing a functional bloc including HDL modeling, simulation and logical synthesis targeting FPGAs. Part of the result can be included in larger systems in use for our academic or scientific projects.
- R&D and prototyping for digital data processing and/or embedded board controllers for research projects in the field of High Energy physics and Astrophysics. Our developments mostly consist in digital electronics interfacing mixed signal ASICs and a data acquisition system based on FPGAs. They are featuring digital signal processing, protocol handling, data encapsulation and board control logic.

I confirm that the end usage will be strictly non-commercial in its nature and within the terms of the Research Laboratory End User Agreement that my Institution will have in place at the time of purchasing these tools via EUROPRACTICE.

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Sous la co-tutelle de

